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				Application Number	
				Filing Date	
				First Named Inventor	Hezar et al.
				Group Art Unit	
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-36192

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
✓	AA	5,065,157		Ribner et al.	11/12/1991	Entire Document
	AB	5,103,229		Ribner	04/07/1992	Entire Document
	AC	5,181,032		Ribner	01/19/1993	Entire Document
		5,283,578		Ribner et al.	02/01/1994	Entire Document
	AD	5,305,004		Fattaruso	04/19/1994	Entire Document
		5,500,645		Ribner et al.	03/19/1996	Entire Document
		5,952,947		Nussbaum et al.	09/14/1999	Entire Document
		6,148,048		Kerth et al.	11/14/2000	Entire Document
	AF	6,611,221	B1	Soundarapandian et al.	08/26/2003	Entire Document
✓		6,642,874	B1	Lin et al.	11/04/2003	Entire Document

Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
✓	CA	"A Direct Digital Synthesizer with a tunable Error Feedback Structure", JOUKO JANKKA, IEEE Transactions on Communications, Vol. 45, No. 4, April, 1997, pp. 416-420.	
	CB	"A Noise-Shaping Coder Topology for 15+ Bit Converters", L. RICHARD CARLEY, IEEE Journal of Solid-State Circuits, Vol. 24, No. 2, April, 1989, pp. 267-273.	
	CC	"Multibit $\Sigma$ - $\Delta$ A/D Converter Incorporating A Novel Class of Dynamic Element Matching Techniques", BOSCO H. LEUNG and SCHAT SUTARJA, IEEE Transactions on Circuits and Systems, Analog and Digital Signal Processing, Vol. 39, No. 1, January, 1992, pp. 35-51.	
	CD	"A 1.5V 2.4/2.9mW 79/50dB DR Sigma Delta Modulator for GSM/WCDMA in a 0.13um Digital Process", G. GOMEZ and B. HAROUN, Solid State Circuits Conference, Digest of Technical Papers, Vol. 1, pp. 306-307, February 2002 San Francisco, CA, and Visuals Supplement, pp. 242, 243, 490.	
	CE	Institute of Electrical and Electronics Engineers, 2002 Digest of Technical Papers and Visuals Supplement 2002 to the Digest of Technical Papers, IEEE International Solid-State Circuits conference, Volume Forty-Five, ISSN 0193-6530, 13.3 "A Dual-Mode 80MHz Bandpass $\Delta\Sigma$ Modulator for a GSM/WCDMA IF-Receiver" T. SALO, T. HOLLMAN, S. LINGFORS and K. HALONEN, 7 pgs.	
✓	CF	"Behavioral Modeling of Switched-Capacitor Sigma-Delta Modulators", PIERO MALCOVATI, SIMONA BRIGATI, FABRIZIO FRANCESCONI, FRANCO MALOBERTI, PAOLO CUSIANATO and ANDREA BASCHIROTTI, IEEE Transactions on Circuits and Systems; Fundamental Theory and Applications, Vol. 50, No. 3, March, 2003, pp. 352-364.	

Examiner Signature		Date Considered	4/1/04
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